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PATENT  
FAIR-07500

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Up  
#9

In re Application of  
Ara Bicakaci

Patent No. 6,741,289 B1  
Serial No. 09/703,517

Issue Date: May 25, 2004  
Filed: October 31, 2000

For: **TECHNIQUE TO STABILIZE  
THE CHROMINANCE  
SUBCARRIER GENERATION  
IN A LINE-LOCKED DIGITAL  
VIDEO SYSTEM**

Group Art Unit: 2611

Examiner: Victor R. Kostak

**SECOND REQUEST FOR  
CERTIFICATE OF CORRECTION OF  
PATENT FOR APPLICANT MISTAKE  
UNDER 37 CFR §1.323**

162 North Wolfe Road  
Sunnyvale, CA 94086  
(408) 530-9700

Customer No. 28960

**Attn: Decision and Certificate of Correction  
Branch of the Patent Issue Division**

Commissioner for Patents  
P.O. Box 1450  
Arlington, VA 22313-1450

**Certificate  
AUG 25 2004  
of Correction**

Sir:

Attached in duplicate is Form PTO-1050 with at least one copy being suitable for printing.

This is a second request for a certification of correction for applicant mistake. The first request was returned with the enclosed copy of the Notification Of Return Of Papers. However, the check in the amount of \$100.00 in accordance with 37 CFR 1.20(a) was not returned with this notification. Therefore, the Applicant is **not** enclosing a new check herewith.

The exact page and line number where the errors are shown incorrectly in the application file are:

In the ASSIGNEE of the PTOL-85B filed April 12, 2004:

Delete "FAIRCHILD SEMICONDUCTORS, INC." and insert --FAIRCHILD SEMICONDUCTOR, INC.--.

The PTO did not receive the following listed item(s) _____
--

In the DRAWINGS of the Application filed October 31, 2000:

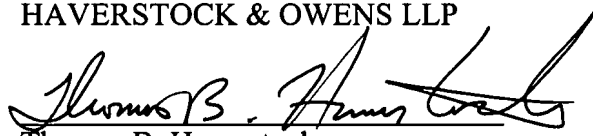
Delete the drawings as filed and replace with formal drawing sheets 1-5.

Respectfully submitted

HAVERSTOCK & OWENS LLP

Dated: 8-16-04

By:

A handwritten signature in black ink, appearing to read "Thomas B. Haverstock", written over a horizontal line.

Thomas B. Haverstock  
Registration No.: 32,571  
Attorneys for Applicant(s)

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO.

: 6,741,289 B1

DATED

: May 25, 2004

INVENTOR(S)

: BicaKci

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

The title page showing the illustrative figure should be deleted to be replaced with the attached title page.

Drawing sheets, consisting of Figs. 1-7, should be deleted to be replaced with the drawing sheets, consisting of Figs. 1-7, as shown on the attached pages.

*On the title page item [73],*

Delete "FAIRCHILD SEMICONDUCTORS, INC." and insert -FAIRCHILD SEMICONDUCTOR, INC.--.

*A*



US006741289B1

(12) **United States Patent**  
**Bicakci**

(10) **Patent No.: US 6,741,289 B1**  
 (45) **Date of Patent: May 25, 2004**

(54) **TECHNIQUE TO STABILIZE THE CHROMINANCE SUBCARRIER GENERATION IN A LINE-LOCKED DIGITAL VIDEO SYSTEM**

(75) **Inventor: Ara Bicakci, San Jose, CA (US)**

(73) **Assignee: Fairchild Semiconductors, Inc., South Portland, ME (US)**

(\*) **Notice:** Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 631 days.

(21) **Appl. No.: 09/703,517**

(22) **Filed: Oct. 31, 2000**

(51) **Int. Cl.<sup>7</sup> H04N 9/45**

(52) **U.S. Cl. 348/505; 348/194; 348/508**

(58) **Field of Search 348/194, 505, 348/508, 512, 549; 386/41, 13, 17, 19, 20**

(56) **References Cited**

#### U.S. PATENT DOCUMENTS

4,370,672 A	*	1/1983	Ross et al.	358/312
4,639,780 A	*	1/1987	Willis	358/150
4,694,327 A	*	9/1987	Demmer et al.	358/19
4,697,207 A	*	9/1987	Lilley	358/17
4,718,074 A		1/1988	Mannas et al.	375/118

4,987,491 A		1/1991	Kaite et al.	358/166
4,989,073 A	*	1/1991	Wagner	358/19
5,132,554 A		7/1992	Hiramatsu et al.	307/269
5,179,438 A	*	1/1993	Morimoto	358/17
5,280,345 A		1/1994	Uehara et al.	358/37
5,303,061 A		4/1994	Matsumoto et al.	358/320
5,359,366 A		10/1994	Ubukata et al.	348/536
5,497,200 A		3/1996	Otaki et al.	348/497
5,557,335 A		9/1996	Oh	348/512
5,796,796 A		8/1998	Wang	375/372
5,877,640 A		3/1999	Geerlings	327/141
5,999,226 A		12/1999	Choi	348/564
6,256,003 B1		7/2001	Tsuchiya et al.	345/87
6,310,653 B1	*	10/2001	Malcolm, Jr. et al.	348/537
6,363,129 B1		3/2002	Agazzi	375/355

\* cited by examiner

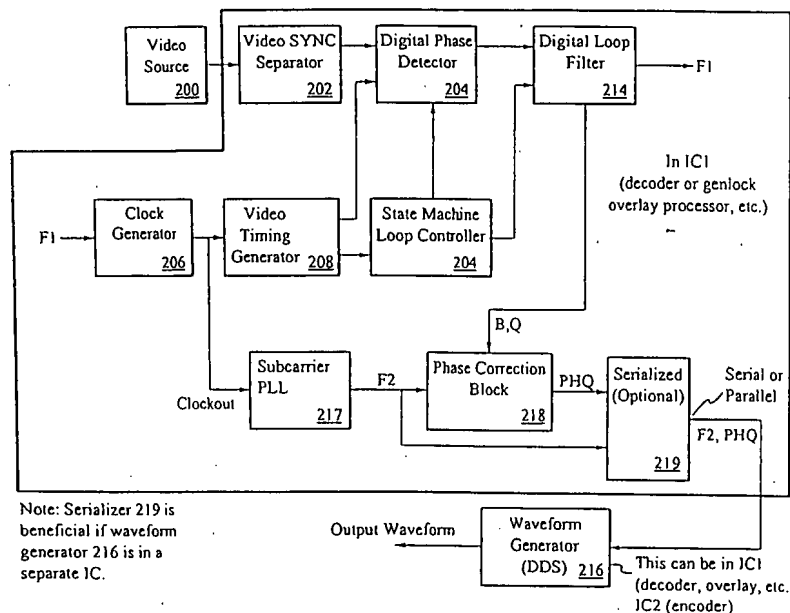
**Primary Examiner—Victor R. Kostak**

(74) **Attorney, Agent, or Firm—Haverstock & Owens LLP**

(57) **ABSTRACT**

A technique to stabilize subcarrier generation in a line-locked digital video system, caused by simultaneous locking of the genlock device causing continuous changing of a shared clock signal, by calculating a time shift occurring in an output waveform, converting the time shift into an equivalent phase shift and sending a corresponding phase correction number to a waveform generator block to correct the time shift, and thus stabilize subcarrier generation.

**45 Claims, 5 Drawing Sheets**



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## CERTIFICATE OF CORRECTION

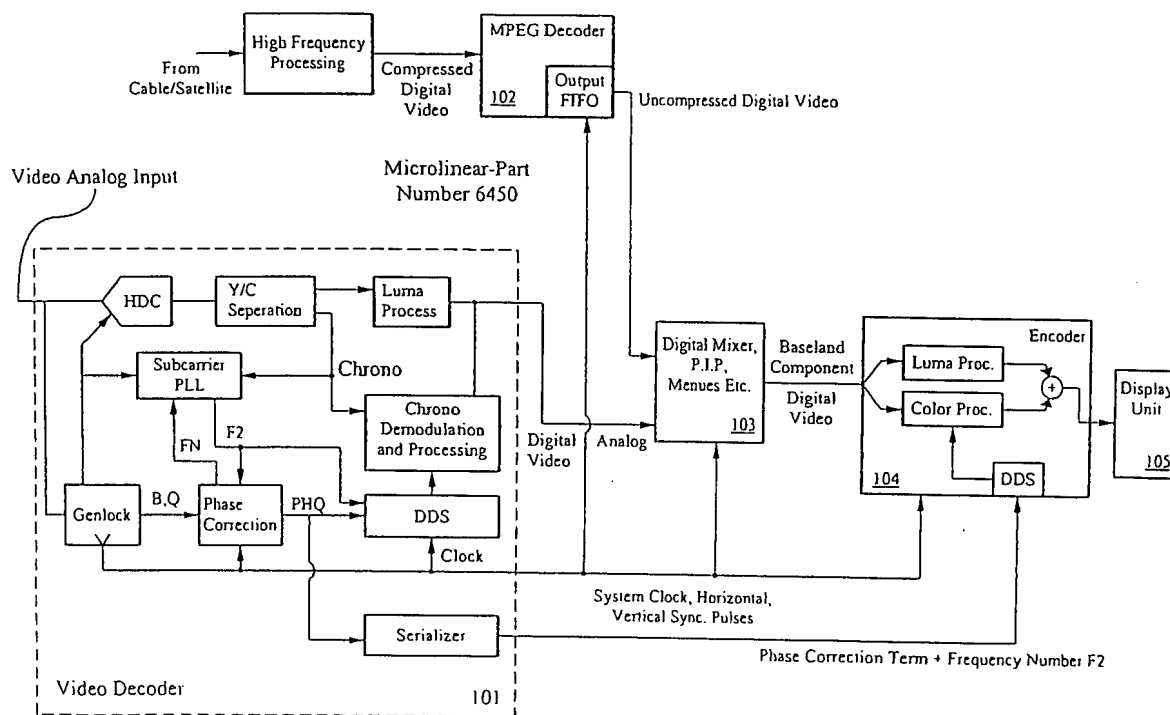
PATENT NO. : 6,741,289 B1

DATED : May 25, 2004

INVENTOR(S) : Ara Bicakci

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby correct as shown below:

IN DRAWING SHEET 1 - REPLACE SHEET 1 AS FILED WITH:



*Fig. 1*

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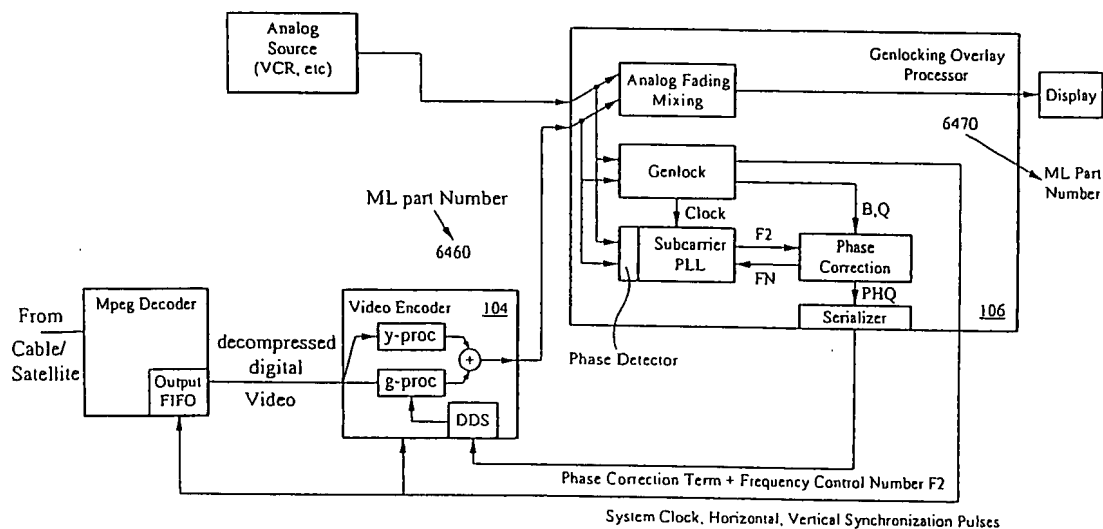
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DATED : May 25, 2004

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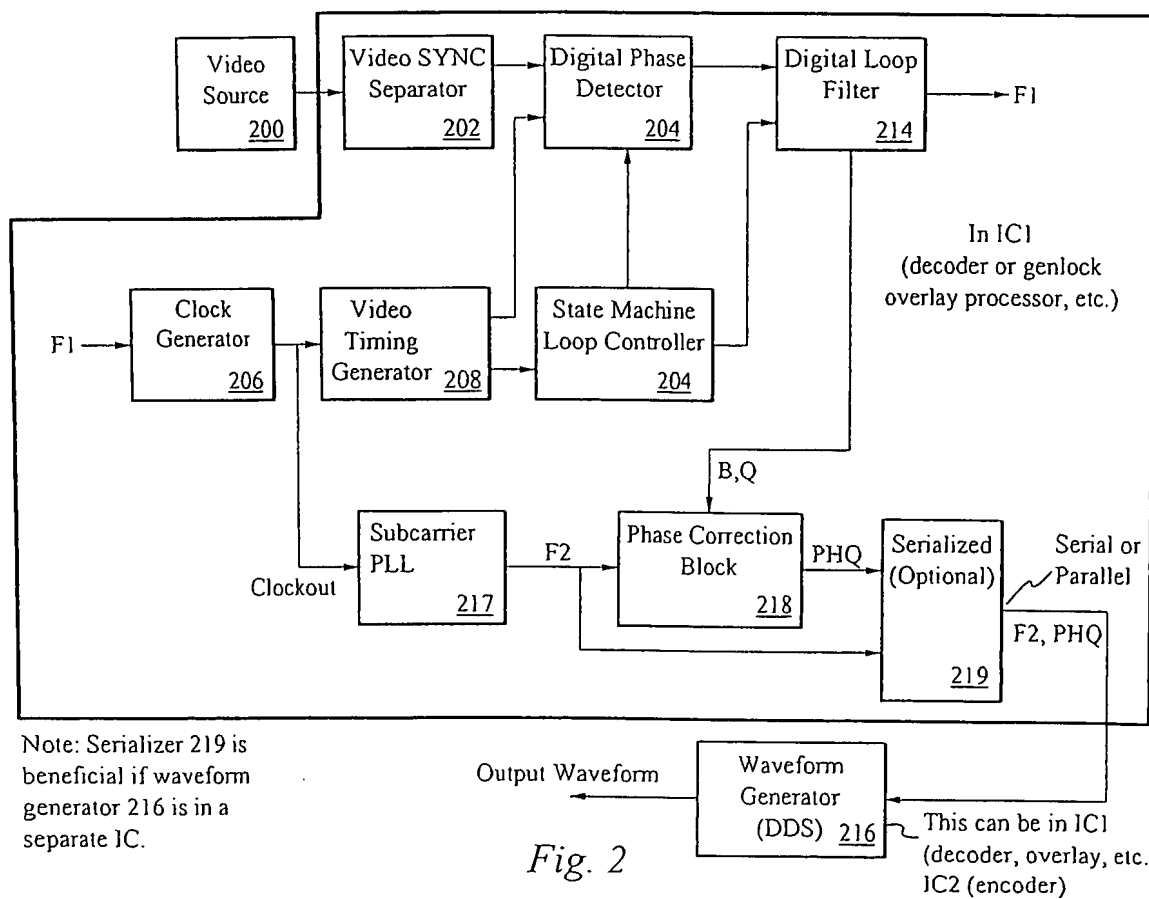
PATENT NO. : 6,741,289 B1

DATED : May 25, 2004

INVENTOR(S) : Ara Bicakci

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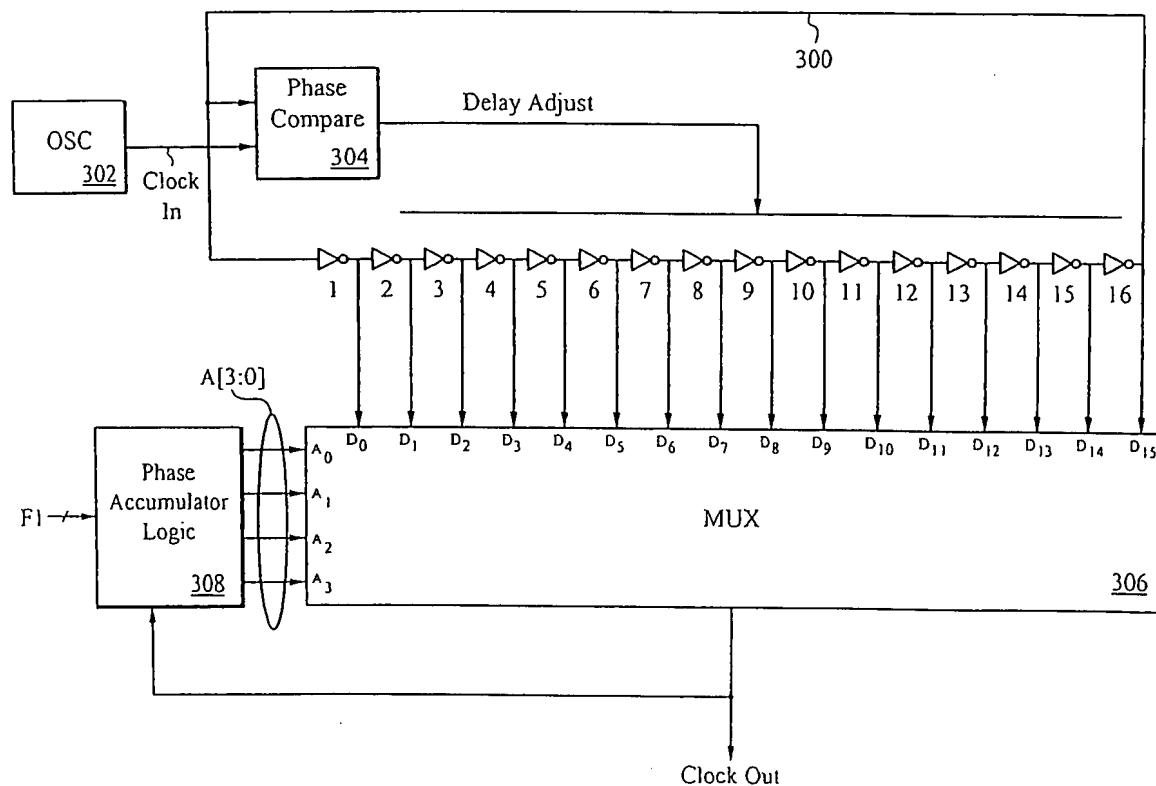
PATENT NO. : 6,741,289 B1

DATED : May 25, 2004

INVENTOR(S) : Ara Bicakci

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